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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/577,547	05/24/2000	Cornelis G.M. Van Asma	PHn 17,450	5029

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EXAMINER
TRAN, TRANG U

ART UNIT	PAPER NUMBER
2614	8

DATE MAILED: 03/11/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/577,547	Applicant(s) VAN ASMA, CORNELIS G.M.	
	Examiner Trang U. Tran	Art Unit 2614	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *26 December 2002* .

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-10* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-10* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)☒ The proposed drawing correction filed on *26 December 2002* is: a)☒ approved b)☐ disapproved by the Examiner

If approved, corrected drawings are required in reply to this Office action.

12)☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____ .

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) ☐ The translation of the foreign language provisional application has been received.

15)☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .
4) ☐ Interview Summary (PTO-413) Paper No(s). _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: .

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed Dec. 26, 2002 have been fully considered but they are not persuasive.

In re pages 2-3, applicant argues that Werner fails to teach a memory manager wherein "an output of the processing means is coupled to an input of the memory manager, and in that the memory manager is further arranged to transfer the output data signal from the processing means to the memory means for storing the output data signal, and to transfer the stored output data signal from the memory means to the output device" recited in claim 1 because Werner's memory manager 22 does not transfer the decompressed and re-sized data output by the scaling engine 25 from the shared memory 23 to an output device but, rather, in Werner, the decompressed and re-sized data output by the scaling engine 25 is provided directly to the picture quality unit 14 by the scaling engine 25, without first being stored in the shared memory 23 (see, e.g., FIGS. 1-3; col. 7, lines 34-35).

In response, the examiner respectfully disagrees. First at all, claim 1 does not recite the alleged that the memory is used to store **the decompressed and re-sized data**. The specification is not the measure of invention. Therefore, limitations contained therein can not be read into the claimed for the purpose of avoiding the prior art. In re Spork, 55 CCPA 743, 386 F.2d 924, 155 USPQ 687 (1968).

Secondly, claim 1 only recites "an output of the processing means is coupled to an input of the memory manager, and in that the memory manager is further arranged

to transfer the output data signal from the processing means to the memory means for storing the output data signal, and to transfer the stored output data signal from the memory means to the output device". Werner discloses in col. 3, lines 5-9 that "Display system 10 comprises a signal interface 11, a video processing unit 13, a picture quality unit 14, a frame buffer 15, a spatial light modulator (SLM) 16, a color wheel 17, a display optics unit 18, and a timing unit 19" and in col. 4, lines 38-46 that "FIG. 2 is block diagram of digital video processing unit 13. As explained below, video processing unit 13 combines decompression and image resizing operations. Separate processing "engines" 24 and 25 perform these operations, first decompression and then resizing. These engines 24 and 25 share a memory 23 that stores the data to be operated on. A request-driven memory manager 22 handles the tasks of supplying the proper data at the proper time to each engine 24 and 25". It is clear from the above passages, **the claimed processing means is anticipated by the signal interface 11 of Werner, the claimed memory manager is anticipated by the memory 23 of Werner and the claimed output device is anticipated by the picture quality unit 14 of Werner because the memory 23 of Werner stores the output of the signal interface 11 and transfer the stored output signal to the picture quality unit 14.**

Finally, **alternatively**, Werner discloses from col. 4, line 63 to col. 5, line 7 that "Memory manager 22 receives the compressed video data and stores it in memory 23, Memory manager 22 handles the task of providing the appropriate data at the appropriate times to decoding engine 24 and scaling engine 25. The specific tasks of memory manager 22 are to: extract compressed data from interface 11 and place it in

memory 23, move compressed data from memory 23 to decoding engine 24, move decompressed data from decoding engine 24 back into memory 23, move decompressed data from memory 23 to scaling engine 25, and arbitrate any conflicting requests for data from the decoding engine 24 and the scaling engine 25". From the above passage, **the claimed processing means is anticipated by the signal interface 11 of Werner, the claimed memory manager is anticipated by the memory 23 of Werner and the claimed output device is anticipated by the decoding engine 24 of Werner because the memory 23 of Werner stores the output of the signal interface 11 and transfer the stored output signal to the decoding engine 24** or **the claimed processing means is anticipated by the decoding engine 24 of Werner, the claimed memory manager is anticipated by the memory 23 of Werner and the claimed output device is anticipated by the scaling engine 25 of Werner because the memory 23 of Werner stores the output of the decoding engine 24 and transfer the stored output signal to the scaling engine 25**. Thus, Werner does disclose the alleged limitations of claim 1.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Werner, William B. (EP 0 840 522).

In considering claim 1, Werner, William B. discloses all the claimed subject matter, note 1) the claimed processing means for processing the input signal to the output data signal is met by the decoding engine 24 (Fig. 2, col. 5, lines 25-51 and col. 6, lines 27-54), 2) the claimed memory means for storing the input signal prior to supply to said processing means is met by the memory 23 (Fig. 2, col. 5, line 52 to col. 6, line 26), and 3) the claimed a memory manager coupled with the processing means and the memory means, the memory manager being arranged to transfer the input data signals to the memory means and to transfer the stored input data signal to the processing means, respectively, characterized in that an output of the processing means is coupled to an input of the memory manager, and in that the memory manager is further arranged to transfer the output data signal from the processing means to the memory means for storing the output data signal, and to transfer the stored output data signal from the memory means to the output device is met by the memory manager 22 (Fig. 2, col. 5, line 52 to col. 6, line 11).

In considering claim 2, the claimed wherein the memory manager is further arranged to transfer the stored data output signal to the processing means, and said processing means is further arranged to execute a further operation on the stored data output signal is met by the scaling engine 25 (Fig. 2, col. 5, line 52 to col. 6, line 11).

In considering claim 3, the claimed wherein said processing means and said memory manager are arranged to execute different processes in time multiplex is met by col. 5, lines 35-56.

In considering claim 4, the claimed wherein said processing means, said memory manager and the memory means are comprised on a single integrated circuit is met by col. 5, lines 35-45.

In considering claim 5, the claimed wherein said processing means is arranged to resize an image represented by said input data signal is met by the scaling engine 25 (Fig. 2, col. 6, line 55 to col. 7, line 40).

In considering claim 6, the claimed wherein said processing means is further arranged to convert said input data signal representing an image into a color-sequential output signal is met by the SLM 16 which generates an image for each of three different colors of a picture and these three images are sequentially displayed through a color wheel 17 (Fig. 1, col. 4, line 38 to col. 5, line 20).

In considering claim 7, the claimed wherein said processing means is arranged to convert said input data signal representing an image into a sub-field modulated output signal for controlling the output device is met by the pulse-width modulation techniques (col. 1, line 13 to col. 2, line 27).

In considering claim 8, the claimed wherein a part of the memory means is arranged as a cyclic memory for storing a part of an image represented by the input data signal is met by the memory 23 (Fig. 2, col. 6, lines 12-26).

In considering claim 9, the claimed wherein the memory manager has a further input for a second data input signal, and the memory manager is arranged to transfer the second data input signal to the memory means is met by the memory manager 22

which receives the control signals from the control interface 21 (Fig. 2, col. 5, lines 46-51 and col. 6, lines 21-26).

In considering claim 10, the claimed a digital video-processing unit having an output for providing the output data signal and a display system coupled to the output of said digital video-processing unit for displaying of said output data signal is met by the display optics 18 and the screen (Fig. 1).

Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Trang U. Tran** whose telephone number is **(703) 305-0090.**

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **John W. Miller,** can be reached at **(703) 305-4795.**

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

TT TT
March 6, 2003

JOHN MILLER
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600